

## **Application of computer simulation for the design of a new high voltage transducer, aiming to high voltage measurements at field, considering frequencies from DC up to 5 kHz**

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*Abstract:* - This paper shows the application of computer simulations using PSPICE program Student version [1], for the development of a new kind of high voltage transducer, considering frequencies from DC up to 5 kHz. Many different approaches and solutions are analyzed, aiming to the damping of voltage oscillations in the capacitive divider output, caused by inductances present in field measurements, besides of the inherent undamped behavior of the capacitive divider.

*Key-Words:* - capacitive divider, high voltage, PSPICE, switching transients, high voltage measurements